ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS

Submision Type : Utility Patent Filing

Application Number:

10/707071

EFS ID: 50888

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fucntion call.

First Named Applicant: Darren Anand

Attorney Docket Number: BUR920030168US1

Timestamp: 2003-11-19 11:38:54 EDT

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File Listing:

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us-assignment	BUR920030168US1-usassn.xml	2182	2003-11-19
us-assignment	us-assignment.xsl	8983	2003-11-19
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us-assignment	BUR920030168Assign1.tif	70348	2003-11-19
us-assignment	BUR920030168Assign2.tif	11008	2003-11-19
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application-body	us-application-body.xsl	83497	2003-11-19
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application-body	wipo.ent	4956	2003-11-19
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